

**CLAIMS:**

1. A power control for a peripheral device insertable into a host device, the power control comprising:
  - 5 a switch configured to generate a signal that simulates the insertion and removal of the peripheral device within the host device such that power from the host device will be supplied to the peripheral device when simulating the device is inserted into the host device and power will be removed from the peripheral device when simulating the peripheral device is removed from the host device;
  - 10 wherein the peripheral device is not physically inserted or removed from the host device.
2. The power control of Claim 1 wherein the switch is configured to generate an inserted signal simulating insertion of the peripheral device and a removed signal simulating removal of the peripheral device.
- 15 3. The power control of Claim 2 wherein the switch is configured to generate the inserted signal upon extension of an antenna of the peripheral device and configured to generate the removed signal upon retraction of the antenna.
- 20 4. The power control of Claim 3 wherein the switch is electrically connected to detecting pins of the host device, the detecting pins determining whether the peripheral device is inserted or removed from the host device.
5. The power control of Claim 4 wherein the switch is operative to generate an open circuit as the removed signal and a low voltage level as the inserted signal.

6. The power control of Claim 5 wherein the low voltage level is a ground potential.

7. The power control of Claim 5 wherein the switch includes a lever which detects the position of the antenna in order to generate the inserted and removed signals.

8. The power control of Claim 7 wherein the peripheral device is a PCMCIA card.

9. A method of controlling power to a peripheral device insertable into a host device, the method comprising the steps of:

10 a) simulating the insertion of the peripheral device with a switch by generating an inserted signal such that the host device supplies power to the peripheral device; and

15 b) simulating the removal of the peripheral device with the switch by generating a removed signal such that the host device removes power from the peripheral device.

10. The method of Claim 9 wherein the switch is in electrical communication with detecting pins of the host device and step (a) further comprises generating the inserted signal on the detecting pin and step (b) further comprises generating the removed signal on the detecting pins.

11. The method of Claim 10 wherein step (a) comprises generating the inserted signal upon extension of an antenna of the peripheral device and step (b) comprises generating the detecting signal upon retraction of the antenna of the peripheral device.

5 12. The method of Claim 11 wherein step (a) comprises generating the inserted signal by forming a low voltage signal on the detecting pin of the host device and step (b) comprises generating the removed signal by forming an open circuit on the detecting pin of the host device.

10 13. The method of Claim 12 wherein the low voltage signal is a ground potential.

14. The method of Claim 12 wherein step (a) comprises detecting the extension of the antenna with the switch and step (b) comprises detecting the retraction of the antenna with the switch.

15 15. The method of Claim 14 wherein step (a) comprises detecting the extension of the antenna with a lever of the switch and step (b) comprises detecting the retraction of the antenna with the switch.

16. A power control for a peripheral device insertable within a host device, the power control comprising:

20 means for simulating the insertion of the peripheral device into host device; and

means for simulating the removal of the peripheral device from host device.

PATENT  
Atty. Dkt. No. 024938-172

17. The power control of Claim 16 wherein the means for simulating insertion and the means for simulating removal is a switch.

18. The power control of Claim 17 wherein the switch is operative to generate a signal simulating the removal and insertion of the peripheral device.

5 19. The power control of Claim 18 wherein the switch is in electrical communication with a detecting pin of the host device and the switch is operative to generate the signal on the detecting pin.

10 20. The power control of Claim 19 wherein the switch is operative to generate an inserted signal simulating the insertion of the peripheral device and a removed signal simulating the removal of the peripheral device.

21. The power control of Claim 20 wherein the switch is operative to generate an open circuit as the removed signal and a low voltage level as the inserted signal.

15 22. The power control of Claim 21 wherein the low voltage level is a ground potential.

23. The power control of Claim 22 wherein the switch detects the position of the antenna in order to generate the inserted and removed signals.

24. The power control of Claim 23 wherein the switch has a lever which detects the position of the antenna.